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FIG. 1

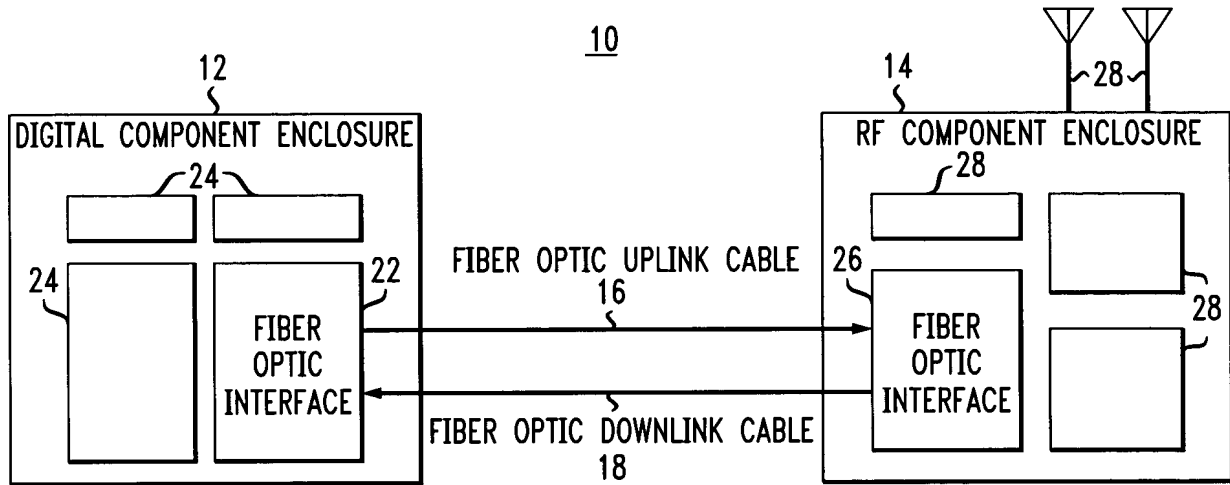


FIG. 2

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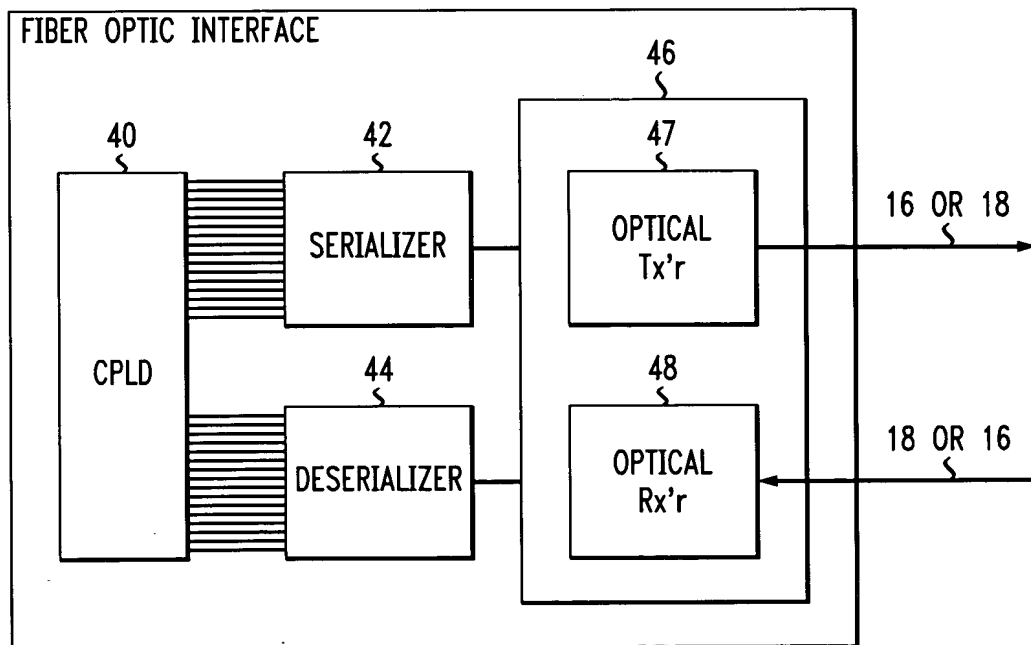


FIG. 3

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DATA	SIGNALS ^a	BIT	DATA RATE	COMMENTS
UP TO RF EQUIPMENT				
Tx DATA BUS	LTX01AI0	0	19.6608 MHz	EACH SECTOR MUXED AT 78.6432 MHz
	LTX01AI1	1	19.6608 MHz	
	LTX01AQ0	2	19.6608 MHz	
	LTX01AQ1	3	19.6608 MHz	
ARCNET BUS	ARCNET_TXEN	10, 11	1.25 Mbps	
EVEN SECOND TICK	EVEN_SEC	12 (TRANSMITTED ON ONE MULTIPLEXED CHANNEL)	19.6608 MHz	1 PULSE EVERY 2 SEC, 19.6608 MHz RESOLUTION
RADIO SERIAL PORT	CBRSER		LOW	TO PCBR
SERIAL PORT FROM RADIO CONTROLLER	CRC SER		LOW	RESERVED FOR FUTURE USE
RADIO RF EMERGENCY SWITCH	CBR_SW		STATIC	TO PCBRs; ONE TO PCBR IN EACH RFU
CCU AVAILABLE	CAVAIL_N		STATIC	STATIC, TRANSMIT IN CONTROL CHANNEL. ONE TO EACH OF THREE RFUs
SPARE	-		-	
SPARE	-		-	
SPARE	-		-	
PHYSICAL ADDRESS	RF ENCLOSURE_ID (3:0)	13 (TRANSMITTED ON ONE MULTIPLEXED CHANNEL)	STATIC	4 UNIQUE BITS TO EACH RFU
MULTIPLEX SYNC PULSE		14	2.4576 Mbps	
SPARE		4	-	RESERVED FOR FUTURE USE
SPARE		5	-	RESERVED FOR FUTURE USE
SPARE		6	-	RESERVED FOR FUTURE USE
SPARE		7	-	RESERVED FOR FUTURE USE
SPARE	-	8	-	RESERVED FOR FUTURE USE
SPARE	-	9	-	RESERVED FOR FUTURE USE
PARITY	-	15	-	PARITY CHECK
19.6608 MHz CLOCK				NOT SENT; SCLK23 CLOCK IS RECOVERED IN RF ENCLOSURE; SCLK34 IS THEN GENERATED FROM SCLK23

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FIG. 4

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SLOT/BIT	0	1	2	3	4	5	6	7	8	9
DATA	LTX01AI0	LTX01AI1	LTX01AQ0	LTX01AQ1	SPARE	SPARE	SPARE	SPARE	SPARE	SPARE

SLOT/BIT	10	11	12	13	14	15	16	17	18	19
DATA	ARCNET_EN	ARCNET	MUX	MUX	MUX SYNC	PARITY		RESERVED FOR FOM USE		

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FIG. 5

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DATA	SIGNALS ^a	BIT	DATA RATE	COMMENTS
DOWN TO BBU				
RXDATA	RXIA0	0	19.6608 MHz	DIV0/DIV1 ARE MULTIPLEXED IN DATA STREAM
	RXIA1	1	19.6608 MHz	
	RXIA2	2	19.6608 MHz	
	RXIA3	3	19.6608 MHz	
	RXQA0	4	19.6608 MHz	
	RXQA1	5	19.6608 MHz	
	RXQA2	6	19.6608 MHz	
	RXQA3	7	19.6608 MHz	
ARCNET	ARCNET_RX	11	1.25 Mbps	
BYPASS FROM RADIO	CLBYP_N	12 (ONE MUXED CHANNEL)	STATIC	FROM RADIO
SERIAL PORT TO CDMA RADIO CONTROLLER	CRC SER		LOW	
RADIO SERIAL PORT	CBRSER		LOW	
SPARE	-		-	RESERVED FOR FUTURE USE
SPARE	-		-	RESERVED FOR FUTURE USE
SPARE	-		-	RESERVED FOR FUTURE USE
SPARE	-		-	RESERVED FOR FUTURE USE
SPARE	-		-	RESERVED FOR FUTURE USE
DEMULTIPLEXER SYNC		14	2.4576 Mbps	
PARITY	-	15	-	PARITY CHECK
SPARE	-	8	-	RESERVED FOR FUTURE USE
SPARE	-	9	-	RESERVED FOR FUTURE USE
SPARE	-	10	-	RESERVED FOR FUTURE USE
SPARE	-	13	-	RESERVED FOR FUTURE USE

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FIG. 6

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SLOT/BIT	0	1	2	3	4	5	6	7	8	9
DATA	RXIA0	RXIA1	RXIA2	RXIA3	RXQA0	RXQA1	RXQA2	RXQA3	SPARE	SPARE

SLOT/BIT	10	11	12	13	14	15	16	17	18	19
DATA	SPARE	ARCNET	MUX	MUX	MUX SYNC	PARITY	RESERVED FOR FOM USE			